Lattice-based Zero-knowledge SNARGs for Arithmetic Circuits

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Abstract. Succinct non-interactive arguments (SNARGs) enable verifying NP computations with substantially lower complexity than that required for classical NP verification. In this work, we construct a zeroknowledge SNARG candidate that relies only on lattice-based assumptions which are claimed to hold even in the presence of quantum computers.

Central to our construction is the notion of linear-targeted malleability introduced by Bitansky et al. (TCC 2013) and the conjecture that variants of Regev encryption satisfy this property. Then, using the efficient characterization of NP languages as Square Arithmetic Programs we build the first quantum-resilient zk-SNARG for arithmetic circuits with a constant-size proof consisting of only 2 lattice-based ciphertexts. Our protocol is designated-verifier, achieves zero-knowledge and has shorter proofs and shorter CRS than the previous such schemes, e.g. Boneh et al. (Eurocrypt 2017).

Keywords: lattice-based \cdot zero-knowledge \cdot SNARG \cdot post-quantum

1 Introduction

1.1 Zero-Knowledge Arguments

Zero-knowledge arguments are cryptographic protocols between two parties, a prover P and a verifier V, in which the prover can convince the verifier about the validity of a statement without leaking any extra information beyond the fact that the statement is true.

Since their introduction in [25] zero-knowledge (ZK) proofs have been shown to be a very powerful instrument in the design of secure cryptographic protocols.

Related to efficiency and to optimization of communication complexity, it has been shown that statistically-sound proof systems are unlikely to allow for significant improvements in communication [13, 23, 24, 42]. When considering proof systems for NP this means that, unless some complexity-theoretic collapses occur, in a statistically sound proof system any prover has to communicate, roughly, as much information as the size of the NP witness. The search for ways to beat this bound motivated the study of *computationally-sound* proof systems, also called *argument systems* [15], where soundness is required to hold only against *computationally bounded* provers.

1.2 SNARG: Succinct Non-Interactive Arguments

Assuming the existence of collision-resistant hash functions, Kilian [30] showed a four-message interactive argument for NP. In this protocol, membership of an instance x in an NP language can be proven with communication and verifier's running time significantly smaller than required in the classical NP verification. Argument systems of this kind are called *succinct*. A challenge, which is of both theoretical and practical interest, is the construction of non-interactive succinct arguments. Starting from Kilian's protocol that requires four messages, Micali [35] used the Fiat-Shamir heuristic [17] to construct a *one-message* succinct argument for NP whose soundness is set in the random oracle model.

In the plain model, a non-interactive argument requires the verifier V (or a trusted party) to generate a common reference string **crs** ahead of time and independently of the statement to be proved by the prover P. Such systems are called *succinct non-interactive arguments* (SNARGs) [22]. Several SNARGs constructions have been proposed [26, 33, 8, 19, 39, 16, 27], and the area of SNARGs has become popular in the last years with the proposal of constructions which introduced significant improvements in efficiency. Many of these SNARGs are also *arguments of knowledge* – so called SNARKs [8, 7].

In parallel with improvements in efficiency, there has been interesting work on understanding SNARGs. An important remark is that all such constructions are based on non-falsifiable assumptions [38], a class of assumptions that is likely to be inherent in proving the security of SNARGs for general NP languages (without random oracles), as shown by Gentry and Wichs [22]. Bitansky et al. [8] proved that designated verifier SNARKs exist if and only if extractable collision-resistant hash functions exist. Bitansky et al. [9] give an abstract model of SNARKs that rely on linear encodings of field elements. Their information theoretic framework called linear interactive proofs (LIPs) capture proof systems where the prover is restricted to using linear operations in computing the expected proof. They give a generic conversion of a 2-move LIP to a publicly verifiable SNARK using pairing-based techniques or to a designated verifier SNARK using additively homomorphic encryption techniques.

1.3 SNARGs for Arithmetic Circuits

The methodology for building SNARGs common to a family of constructions, some of which represent the state of the art [39, 34, 16, 27, 20], has as a central starting point the framework based on quadratic programs introduced by Gennaro et al. in [19]. This common framework allows to build SNARGs and SNARKs for programs instantiated as boolean or arithmetic circuits.

This approach has led to fast progress towards practical verifiable computations. For instance, using span programs for arithmetic circuits (QAPs), Pinocchio [39] provides evidence that verified remote computation can be faster than local computation. At the same time, their construction is zero-knowledge, enabling the server to keep intermediate and additional values used in the computation private.

1.4 Post-Quantum SNARGs

Most of the SNARGs constructed so far are based on discrete-logarithm type assumptions, that do not hold against quantum polynomial-time adversaries [41], hence the advent of general-purpose quantum computers would render insecure the constructions based on these assumptions. Efforts were made to design such systems based on quantum resilient assumptions. We note that the original protocol of Micali [35] is a zk-SNARG which can be instantiated with a post-quantum assumption since it requires only a collision-resistant hash function – however (even in the best optimized version recently proposed in [6]) the protocol does not seem to scale well for even moderately complex computations.

Some more desirable assumptions that withstand quantum attacks are the lattice assumptions [1, 37]. Nevertheless, few non-interactive proof systems are built based on lattices. Some recent works that we can mention are the NIZK constructions for specific languages, like [31, 32, 5] and the two designated verifier SNARG constructions [10, 11], designed by Boneh et al. using encryption schemes instantiated with lattices. A similar approach is used by [20] to design a designated-verifier zk-SNARK (that is a SNARG of knowledge) for boolean circuits.

We attempt to make a step forward in this direction by building a designatedverifier zk-SNARG from quantum-resilient assumptions with better efficiency and succinctness than previous such schemes.

1.5 Our Contribution

We introduce in this work a new lattice-based designated-verifier zk-SNARG.

Our scheme uses as a main building block encodings that rely on the Learning With Errors (LWE) assumption, more precisely, we employ a variant of the encryption scheme proposed by Regev in 2005 [40]. We further assume linear-only properties of this lattice encryption scheme conjectured before by [10, 11].

The underlying relation of our zk-SNARG is a square arithmetic program, which is a very efficient characterization of arithmetic circuits. Square arithmetic programs are closely related to quadratic arithmetic programs [19], but use only squarings instead of arbitrary multiplications. As suggested by Groth [27] the use of squarings give nice symmetry properties and a more compact proof. This efficient language allow us to build a zk-SNARG that achieves better succinctness, CRS size and verification time than the previous similar schemes.

We provide a generalization to our scheme, in the spirit of [19, 20], by using encoding schemes with certain properties. We achieve the most compact proofs known to date, consisting in just 2 lattice-based encodings and verification time in the size of the arithmetic circuit representing the statement. This contribution is of independent interest and consists in a generic framework for SNARGs from Square Arithmetic Programs (SAPs). The stronger notion of knowledge soundness (which leads to zk-SNARKs) can be achieved by replacing the linear-targeted malleability property of our encoding schemes with a stronger (extractable) assumption [9].

1.6 Related Work

Recently, in two companion papers [10, 11], Boneh et al. provided the first designated-verifier SNARGs construction based on lattice assumptions.

The first paper [10] has two main results: an improvement on the LPCP construction in [9] and a construction of linear-only encryption based on LWE. The second paper [11] presents a different approach where the information-theoretic LPCP is replaced by a LPCP with multiple provers, which is then compiled into a SNARG again via linear-only encryption. The main advantage of this approach is that it reduces the overhead on the prover, achieving what they call quasi-optimality¹.

Then, [20] exploits the square span program language for boolean circuits in order to introduce a general-purpose framework for SNARGs that can accomodate lattice-based encodings. The main improvements over the previous lattice-based SNARGs showed by [20] are zero-knowledge property and knowledge soundness, this being the first construction of a lattice-based zk-SNARK.

1.7 Techniques and Comparison to Other SNARGs

Our new framework for building SNARGs exploits the advantages of previous proposals taking the best of these approaches. It uses the simple and efficient representation of a arithmetic circuit satisfiability problem, SAP and minimizes the proof size. Also, our scheme does need only plausible hardness assumptions for the underlying encoding scheme for proving computational soundness.

Although conceptually similar to the recent scheme by Gennaro et al. [20], our construction is designed for arithmetic circuits and achieves better properties and efficiency:

SNARG for Arithmetic Circuit Satisfiability. In contract to previous latticebased constructions, designed for boolean circuit satisfiability, our SNARG is built for proving satisfiability of arithmetic circuits which makes it a better candidate for practical applications.

Standard results show that polynomially sized arithmetic circuits are equivalent (up to a logarithmic factor) to Turing machines that run in polynomial time, though of course the actual efficiency of computing via circuits versus on native hardware depends heavily on the application; for example, an arithmetic circuit for matrix multiplication adds essentially no overhead, whereas a boolean circuit for integer multiplication is far less efficient.

While we describe a SNARG for arithmetic circuit satisfiability (over a field $\mathbb{F} = \mathbb{Z}_p$), the problem of boolean circuit satisfiability easily reduces to arithmetic circuit satisfiability with only constant overhead (see [9] Claim A.2).

We remark also that we compare well in terms of efficiency with the quasioptimal SNARG of [10, 11] in the case of arithmetic circuit satisfiability over

¹ This is the first scheme where the prover does not have to compute a cryptographic group operation for each wire of the circuit, which is instead true e.g., in QSP-based protocols.

large fields $(\mathbf{Z}_p, \text{ where } p = 2^{\lambda})$.² In this case, their proof system is no longer a SNARG (not quasi-optimally succinct).

SAP Language for Arithmetic Circuits. Our scheme exploits the simplicity of Square Arithmetic Program to optimize the size of the proofs. Due to their conceptual simplicity, SAPs offer several advantages over previous constructions for arithmetic circuits. Their reduced number of constraints lead to smaller programs, and to lower sizes and degrees for the polynomials required to represent them, which in turn reduce the computation complexity required in SNARG schemes. Notably, their simpler "square" form requires only a single polynomial to be evaluated for verification (instead of two for earlier QSPs, and three for QAP) leading to a simpler and more compact setup, smaller **crs**, and fewer operations required for proof and verification.

Long-Standing Assumptions. Another simplification is the use of the more general long-standing assumption of *linear-targeted malleability* of the encoding (see Section 5.1 for details) instead of the recent introduced *knowledge of exponent* assumptions for lattice encodings of [20]. The soundness of our SNARG is based on a plausible intractability assumption, which is in the spirit of assumptions on which previous SNARGs were based. Moreover, with minimal modifications, based on a stronger variant of the assumption, we can get a SNARK (i.e., a SNARG of knowledge) with similar complexity.

Designated-verifier. One limitation is that our new constructions are designated-verifier, while existing constructions are publicly verifiable.

2 Definitions

2.1 Notation

Let $\lambda \in \mathbb{N}$ be the computational security parameter, and $\kappa \in \mathbb{N}$ the statistical security parameter. We say that a function is *negligible* in λ , and we denote it by $\operatorname{negl}(\lambda)$, if it is a $f(\lambda) = \mathcal{O}(\lambda^{-c})$ for any fixed constant c. We also say that a probability is *overwhelming* in λ if it is $1 - \operatorname{negl}(\lambda)$.

When sampling uniformly at random the value a from the set S, we employ the notation $a \leftarrow s S$. When sampling the value a from the probabilistic algorithm M, we employ the notation $a \leftarrow M$. We use := to denote assignment. For an ndimensional column vector \mathbf{a} , we denote its *i*-th entry by a_i . In the same way, given a polynomial f, we denote its *i*-th coefficient by f_i . Unless otherwise stated, the norm $\|\cdot\|$ considered in this work is the ℓ_2 norm. We denote by $\mathbf{a} \cdot \mathbf{b}$ the dot product between vectors \mathbf{a} and \mathbf{b} .

Unless otherwise specified, all the algorithms defined throughout this work are assumed to be probabilistic Turing machines that run in time $poly(\lambda)$ - i.e., PPT. An adversary is denoted by \mathcal{A} .

² Quasi-optimal succinctness refers to schemes where the argument size is quasilinear in the security parameter

2.2 Succinct Non-Interactive Arguments

In this section we provide formal definitions for the notion of succinct noninteractive arguments (SNARGs).

A SNARG can be defined for a specific efficiently decidable binary relation \mathcal{R} . Let \mathfrak{R} be a relation generator that given a security parameter λ in unary returns a polynomial time decidable binary relation \mathcal{R} . The relation generator may also output some side information, an auxiliary input z, which will be given to the adversary.

For pairs $(u, w) \in \mathcal{R}$ we call u the statement and w the witness. Let $L_{\mathcal{R}}$ be the language consisting of statements for which there exist matching witnesses in \mathcal{R} .

Definition 1 (zk-SNARG for NP). An efficient prover designated-verifiable non-interactive argument for \mathcal{R} is a quadruple of probabilistic polynomial algorithms $\Pi = (\text{Gen}, P, V, \text{Sim})$ such that:

- $(crs, vrs, td) \leftarrow Gen(1^{\lambda}, \mathcal{R})$ the CRS generation algorithm takes as input some security parameter λ and outputs a common reference string crs, a verification state vrs, and a trapdoor td.
- $\pi \leftarrow \mathsf{P}(\mathsf{crs}, u, w)$ the prover algorithm takes as input the crs , a statement u, and a witness w. It outputs some argument π .
- $b \leftarrow V(vrs, u, \pi)$ the verifier algorithm takes as input a statement u together with an argument π , and vrs. It outputs b = 1 (accept) if the proof was accepted, b = 0 (reject) otherwise.
- $\pi \leftarrow \mathsf{Sim}(\mathsf{crs}, \mathsf{td}, u)$ the simulator takes as input a simulation trapdoor td and a statement u together with a proof π and returns an argument π .

In the same line of past works [16, 18, 20], we will assume for simplicity that crs can be extracted from the verification key vrs, and that the unary security parameter 1^{λ} as well as the relation \mathcal{R} can be inferred from the crs.

Non-interactive proof systems are generally asked to satisfy some security properties that simultaneously protect the prover from the disclosure of the witness, and the verifier from a forged proof. We now state the security notions necessary to define a zk-SNARG:

- **Completeness.** For every relation \mathcal{R} , given a true statement, a honest prover P with a valid witness should convince the verifier V with overwhelming probability. More formally, for all $\lambda \in \mathbb{N}$, for all $\mathcal{R} \leftarrow \mathfrak{R}(1^{\lambda})$ and for all $(u, w) \in \mathcal{R}$:

$$\Pr \begin{bmatrix} \mathsf{V}(\mathsf{vrs}, u, \pi) = 1 \\ \land (u, w) \in \mathcal{R} \end{bmatrix} | \begin{pmatrix} \mathsf{crs}, \mathsf{vrs}, \mathsf{td} \end{pmatrix} \leftarrow \mathsf{Gen}(1^{\lambda}, \mathcal{R}) \\ \pi \leftarrow \mathsf{P}(\mathsf{crs}, u, w) \end{bmatrix} = 1 - \mathsf{negl}(\lambda)$$

- Computational Soundness. An argument system requires that no computationally bounded adversary can make an honest verifier accept a proof of a false statement $u \notin L_{\mathcal{R}}$. More formally, for every PPT adversarial prover \mathcal{A} , for any relation $\mathcal{R} \leftarrow \mathfrak{R}(1^{\lambda})$ there is a negligible function $\mathsf{negl}(\lambda)$ such that:

$$\Pr \begin{bmatrix} \mathsf{V}(\mathsf{vrs}, u, \pi) = 1 \\ \land u \notin L_{\mathcal{R}} \end{bmatrix} (\mathsf{crs}, \mathsf{vrs}, \mathsf{td}) \leftarrow \mathsf{Gen}(1^{\lambda}, \mathcal{R}) \\ (u, \pi) \leftarrow \mathcal{A}(\mathsf{crs}) \end{bmatrix} = \mathsf{negl}(\lambda)$$

- Succinctness. A non-interactive argument where the verifier runs in polynomial time in $\lambda + |u|$ and the proof size is polynomial in λ is called a preprocessing succinct non-interactive argument (SNARG). If we also restrict the common reference string to be polynomial in λ we say the non-interactive argument is a fully succinct SNARG. Bitansky et al. [8] show that preprocessing SNARKs can be composed to yield fully succinct SNARKs. The focus of this paper is on preprocessing SNARKs, where the common reference string may be long.
- Statistical Zero-knowledge. An argument is zero-knowledge if it does not leak any information besides the truth of the statement. Formally, if for all $\lambda \in \mathbb{N}$, for all $\mathcal{R} \leftarrow \mathfrak{R}(1^{\lambda})$, for all $(u, w) \in \mathcal{R}$ and for all PPT adversaries \mathcal{A} the following two distributions are statistically close:

$$D_0 = \left[\pi_0 \leftarrow \mathsf{P}(\mathsf{crs}, u, w) : (\mathsf{crs}, \mathsf{vrs}, \mathsf{td}) \leftarrow \mathsf{Gen}(1^\lambda, \mathcal{R}) \right],$$
$$D_1 = \left[\pi_1 \leftarrow \mathsf{Sim}(\mathsf{crs}, \mathsf{td}, u) : (\mathsf{crs}, \mathsf{vrs}, \mathsf{td}) \leftarrow \mathsf{Gen}(1^\lambda, \mathcal{R}) \right].$$

Adaptive Soundness. A SNARG is called *adaptive* if the prover can choose the statement u to be proved after seeing the reference string **crs** and the argument remains sound.

SNARG vs. SNARK. If we replace the computational soundness with computational *Knowledge Soundness* we obtain what we call a SNARK, a succinct non-interactive argument of knowledge.

- Knowledge Soundness. The notion of knowledge soundness implies that there is an extractor that can compute a witness whenever the adversary produces a valid argument. The extractor gets full access to the adversary's state, including any random coins. Formally, we require that for all PPT adversaries \mathcal{A} there exists a PPT extractor $\varepsilon_{\mathcal{A}}$ such that

$$\Pr \begin{bmatrix} \mathsf{V}(\mathsf{vrs}, u, \pi) = 1 \\ \land (u, w) \notin \mathcal{R} \end{bmatrix} | \begin{pmatrix} \mathsf{crs}, \mathsf{vrs}, \mathsf{td} \end{pmatrix} \leftarrow \mathsf{Gen}(1^{\lambda}, \mathcal{R}) \\ ((u, \pi); w) \leftarrow \mathcal{A} \| \varepsilon_{\mathcal{A}}(\mathsf{crs}) \end{bmatrix} = \mathsf{negl}(\lambda)$$

Publicly verifiable vs. Designated Verifier. We define a SNARG such that the setup algorithm for the argument system also outputs a secret verification state vrs which is needed for proof verification. If adaptive soundness holds against adversaries that also have access to the verification state vrs, then the SNARG is called *publicly verifiable*; otherwise it is *designated verifier*. A key question that arises in the design and analysis of designated verifier arguments is whether the same common reference string can be reused for multiple proofs. Formally, this "multi-theorem" setting is captured by requiring soundness to hold even

against a prover that makes adaptive queries to a proof verification oracle. If the prover can choose its queries in a way that induces noticeable correlations between the outputs of the verification oracle and the secret verification state, then the adversary can potentially compromise the soundness of the scheme. Thus, special care is needed to construct designated-verifier argument systems in the multi-theorem setting.

3 Building Blocks

3.1 Arithmetic Circuits.

Informally, an arithmetic circuit consists of wires that carry values from a field \mathbb{F} and connect to addition and multiplication gates.

We designate some of the input/output wires as specifying a statement and use the rest of the wires in the circuit to define a witness. This gives us a binary relation \mathcal{R} consisting of statement wires and witness wires that satisfy the arithmetic circuit, i.e., make it consistent with the designated input/output wires.

3.2 Square Arithmetic Programs

We characterize NP as Square Arithmetic Programs (SAPs) over some field \mathbb{F} of order $p \geq 2^{\lambda-1}$. SAPs were introduced first by Groth et al. in [28].

The main idea is to represent each gate input and each gate output as a variable. Then we may rewrite each gate as an equation in some variables representing the gate's input and output wires. These equations are satisfied only by the values of the wires that meet the gate's logical specification. By composing such constraints for all the gates in the circuit, a satisfying assignment for any arithmetic circuit can be specified first as a set of quadratic equations, then modified to a square equivalent, and finally, seen as a constraint on the span of a set of polynomials, defining the SAP for this circuit. As a consequence, the prover needs to convince the verifier that all the quadratic equations are satisfiable by finding a solution of the equivalent polynomial problem.

Definition 2 (SAP). A Square Arithmetic Program SAP over the field \mathbb{F} contains two sets of polynomials $\{v_0(x), \ldots, v_m(x)\}, \{w_0(x), \ldots, w_m(x)\} \in \mathbb{F}[x]$ and a target polynomial t(x) such that $\deg(v_i(x)), \deg(w_i(x)) \leq d \coloneqq \deg(t(x))$ for all $i = 0, \ldots, m$.

We say that SAP accepts an input $a_1, \ldots, a_\ell \in \mathbb{F}$ if and only if there exist $\{a_i\}_{i=\ell+1}^m \in \mathbb{F}$ satisfying:

$$t(x) \ divides \ \left(v_0(x) + \sum_{i=1}^m a_i v_i(x)\right)^2 - \left(w_0(x) + \sum_{i=1}^m a_i w_i(x)\right).$$

A SAP with such a description defines the following binary relation \mathcal{R} , where we define $a_0 = 1$,

$$\mathcal{R} = \left\{ \left. (\mathbf{u}, \mathbf{w}) \right| \begin{array}{l} \mathbf{u} = (a_1, \dots, a_{\ell_u}) \\ \mathbf{w} = (a_{\ell_u+1}, \dots, a_m) \\ \exists h(x) \in \mathbb{F}[x], \deg(h(x)) \leq d-2: \\ \left(\sum_{i=0}^m a_i v_i(x) \right)^2 = \sum_{i=0}^m a_i w_i(x) + h(x)t(x) \end{array} \right\}$$

3.3 Encoding Schemes

The main ingredient for an efficient preprocessing SNARG is an encoding scheme E over a field \mathbb{F} with some important properties that allow proving and verifying on top of encoded values. Well-known schemes use deterministic pairing-based encodings, where the values are hidden in the exponent and the security is guaranteed by discrete logarithm type assumptions, e.g. [39, 34, 16, 27]. A formalisation of these encoding schemes was initially introduced in [19]. Here, we recall a variant of this definition that was used for a recent SNARK construction based on lattices in [20]. This definition has the advantage that it accommodates for encodings with noise.

An encoding scheme $E=(\mathsf{K},\mathsf{E})$ over a field $\mathbb F$ is composed of the following algorithms

- $\mathsf{K}(1^{\lambda}) \to (\mathsf{pk}, \mathsf{sk})$: a key generation algorithm that outputs some secret state sk together with some public information pk .
- $\mathsf{E}(a) \to C$: a (non-deterministic) encoding algorithm mapping $a \in \mathbb{F}$ to some encoding space C, such that $\{\{\mathsf{E}(a)\} : a \in \mathbb{F}\}$ partitions C, where $\{\mathsf{E}(a)\}$ denotes the set of the possible evaluations of the algorithm E on a. Depending on the encoding algorithm, E will be either deterministic or not

Depending on the encoding algorithm, E will be either deterministic or not and will require either only the public information pk, or the secret state sk. For our application, it will be the case of sk. To ease notation, we will omit this additional argument.

The above algorithms must satisfy the following properties:

- *d*-linearly homomorphic: there exists a poly(λ) algorithm Eval that, given as input the public parameters pk, a vector of encodings (E(a_1),...,E(a_d)), and coefficients $\mathbf{c} = (c_1, \ldots, c_d) \in \mathbb{F}^d$, outputs a valid encoding of $\mathbf{a} \cdot \mathbf{c}$ where $\mathbf{a} = (a_1, \ldots, a_d)$ with probability overwhelming in λ .
- quadratic root detection: there exists an efficient algorithm that, given some parameter δ (either pk or sk), $\mathsf{E}(a_0), \ldots, \mathsf{E}(a_t)$, and the quadratic polynomial $\mathsf{p} \in \mathbb{F}[x_0, \ldots, x_t]$, can distinguish if $\mathsf{p}(a_1, \ldots, a_t) = 0$. With a slight abuse of notation, we will adopt the writing $\mathsf{p}(\mathsf{ct}_0, \ldots, \mathsf{ct}_t) = 0$ to denote the quadratic root detection algorithm with inputs δ , $\mathsf{ct}_0, \ldots, \mathsf{ct}_t$, and p .
- *image verification:* there exists an efficiently computable algorithm \in that, given as input some parameter δ (again, either pk or sk), can distinguish if an element c is a correct encoding of a field element.

Decoding Algorithm. When using a homomorphic encryption scheme in order to instantiate an encoding scheme, we simply define the *decoding algorithm* **Dec** as the decryption procedure of the scheme. More specifically, since we study encoding schemes derived from encryption functions, quadratic root detection and image verification for designated-verifiers are trivially obtained by using the decryption procedure **Dec** together with the secret key sk.

One-way Encodings. If such a secret state is not needed to perform the quadratic root detection, we will consider $\mathsf{sk} = \perp$ and call it "one-way" or *publicly-verifiable* encoding. At present, the only candidates for such a "one-way" encoding scheme that we know of are based on bilinear groups, where the the bilinear maps support efficient testing of quadratic degrees without any additional secret information.

4 zk-SNARG for Arithmetic Circuits

The idea of our SNARG is simple and follows the common paradigm of many well-known pairing-based constructions. The prover has to convince the verifier that it knows some polynomials, such that a division property between them holds (a solution to SAP problem). Instead of sending the entire polynomials as a proof, it evaluates them in a secret point s (hidden by the encoding) to obtain some scalar values. The verifier, instead of checking a polynomial division, has only to check a division between scalars, which makes the task extremely fast.

4.1 Framework for zk-SNARGs from SAP

In a nutshell, in order to construct succinct proofs of knowledge using our framework, one must use the following building blocks:

- an SAP, a way of "translating" the circuit satisfiability problem into a polynomial division problem, meaning that we reduce the proof of computing a circuit to the proof of a solution to this SAP problem,
- an E encoding scheme that hides scalar values, but allows linear operations on the encodings for the prover to evaluate polynomials, and some quadratic check property for the verifier to validate the proofs,
- a CRS generator that uses this encoding scheme to hide a secret random point s and all the necessary powers of s needed later by the prover to compute polynomial evaluations on s.

Our new framework for building SNARGs exploits the advantages of previous such proposals taking the best of each one. It uses the simple and efficient representation of a arithmetic circuit satisfiability problem, SAP and minimizes the proof size of our SNARG scheme. Also, our scheme does need only plausible hardness assumptions for the underlying encoding scheme for proving computational soundness.

 $\operatorname{Gen}(1^{\lambda}, \mathcal{R})$ $\beta, \delta, s \leftarrow \mathbb{F};$ $(\mathsf{pk},\mathsf{sk}) \leftarrow \mathsf{K}(1^{\lambda})$ $\mathsf{crs} \coloneqq \Big(\, \mathsf{SAP} = \left(\{ v_i(x), w_i(x) \}_{i=0}^m, t(x) \right), \ \mathsf{pk}, \ \mathsf{E} \left(\delta t(s)^2 \right), \ \mathsf{E} \left(\beta t(s) \right),$ $\{\mathsf{E}(\delta s^{i})\}_{i=0}^{d-1}, \{\mathsf{E}(\delta s^{i}t(s))\}_{i=0}^{d-2}, \{\mathsf{E}(\delta w_{i}(s)+\beta v_{i}(s))\}_{i=\ell_{u}+1}^{m}\}$ $\mathsf{td} \coloneqq (\beta, \delta, s)$ vrs := sk; return (vrs, crs, td) P(crs, u, w) $V(vrs, u, \pi)$ $\pi := (A, B)$ $\mathbf{u} := (a_1, \ldots, a_{\ell_n})$ $v_{\text{in}}(x) \coloneqq \sum_{i=0}^{\ell_u} a_i v_i(x)$ $\mathbf{w} \coloneqq (a_{\ell_u+1}, \ldots, a_m)$ $v(x) \coloneqq \sum_{i=0}^{m} a_i v_i(x)$ $w_{\text{in}}(x) \coloneqq \sum_{i=0}^{\ell_u} a_i w_i(x)$ $v_{\mathsf{mid}}(x) \coloneqq \sum_{i > \ell_u} a_i v_i(x)$ $V \coloneqq \mathsf{E}(\beta v_{\mathsf{in}}(s))$ $w(x) \coloneqq \sum_{i=0}^{m} a_i w_i(x)$ $W := \mathsf{E}(\delta w_{\mathsf{in}}(s))$ $w_{\mathsf{mid}}(x)\coloneqq \sum_{i>\ell_u}a_iw_i(x)$ Check $h(x) = (v(x)^{2} - w(x))/t(x)$ $A(A + \beta) = \delta(B + W + V)$ $r \leftarrow \mathbb{F}$ Sim(crs, td, u) $f(\mathbf{w}) \coloneqq \delta w_{\mathsf{mid}}(s) + \beta v_{\mathsf{mid}}(s)$ $q(r) \coloneqq r^2 \delta t(s)^2 + 2r \delta t(s) v(s) + r\beta t(s)$ $\mathsf{td} \coloneqq (\beta, \delta, s), \quad \mu \leftarrow _{\$} \mathbb{F}$ $A := \mathsf{E}\big(\delta v(s) + r\delta t(s)\big)$ $\phi(\mathbf{u}) \coloneqq \delta w_{\rm in}(s) + \beta v_{\rm in}(s)$ $A \coloneqq \mathsf{E}(\delta \mu)$ $B \coloneqq \mathsf{E}(f(\mathbf{w}) + \delta t(s)h(s) + g(r))$ $B := \mathsf{E}\big(\delta\mu^2 + \beta\mu - \phi(\mathbf{u})\big)$ return $\pi \coloneqq (A, B)$ $\pi \coloneqq (A, B)$

Fig. 1. Framework for zk-SNARG from SAP

Efficiency. The proof size is 2 encodings. The common reference string contains a description of \mathcal{R} and implicitly the polynomials in SAP, a public key for an encoding scheme and $m+2d+1-\ell_u$ encodings of field elements. The verification consists of checking one quadratic equation on the encoded values.

The prover has to compute the polynomial h(x). It depends on the relation how long time this computation takes; if it arises from an arithmetic circuit where each multiplication gate connects to a constant number of wires, the relation will be sparse and the computation will be linear in d. The prover also computes the coefficients of the representation $v(x) := \sum_{i=0}^{m} a_i v_i(x)$. Having all these coefficients, the prover applies $m + 2d + 1 - \ell_u$ linear homomorphic operations on the encodings from the given **crs**.

$K(1^{\lambda}, \Gamma)$	$E(\mathbf{s},m) ightarrow ct$	$Dec(\mathbf{s}, (\mathbf{c}_0, c_1)) \to m$
$\mathbf{s} \leftarrow _{\$} \mathbf{Z}_q^n$	$\mathbf{a} \mathop{\leftarrow}\!$	$m \coloneqq (\mathbf{c}_0 \cdot \mathbf{s} + c_1) \mod p$
return s	$\sigma \coloneqq q\alpha; \ e \leftarrow \chi_{\sigma}$	$\mathbf{return} \ m$
	$ct \coloneqq (-\mathbf{a}, \ \mathbf{a} \cdot \mathbf{s} + p e + m)$	

Fig. 2. An encoding scheme based on LWE.

Besides the improvements mentioned above, one of the most remarkable features of this framework is the fact that it can accommodate for lattice-based encodings, meaning that we can use it to obtain a quantum-resilient SNARGs.

4.2 Lattice-based Instantiation

In this section, we describe a possible encoding scheme based on learning with errors (LWE) that will be used as a building block for our post-quantum SNARG scheme.

Lattice-Based Encoding Scheme. In order to instantiate our SNARG encoding, we just use Enc = (K, E, Dec) encryption scheme depicted in figure 2. This is the same encoding used to construct lattice-based SNARKs in [20], a slight variation of the classical LWE cryptosystem initially presented by Regev [40] and later extended in [14].

The encryption scheme Enc is described by parameters $\Gamma := (p, q, n, \alpha)$, with $q, n, p \in \mathbb{N}$ such that (p, q) = 1, and $0 < \alpha < 1$. In the corresponding description of our building block E, the public information is constituted by the LWE parameters $\mathbf{pk} = \Gamma$ and an encoding of m is simply an LWE encryption of m. The LWE secret key constitutes the secret state $\mathbf{sk} = \mathbf{s}$ of the encoding scheme.

Basic Properties. We briefly recall the main properties *Enc* should satisfy as a building block in a SNARG scheme.

- correctness. Let $\mathsf{ct} = (-\mathbf{a}, \mathbf{a} \cdot \mathbf{s} + pe + m)$ be an encoding. Then ct is a valid encoding of a message $m \in \mathbf{Z}_p$ if $e < \frac{q}{2n}$.
- *d*-linearly homomorphicity. Given a vector of d encodings $\mathbf{ct} \in \mathbb{Z}_q^{d \times (n+1)}$ and a vector of coefficients $\mathbf{c} \in \mathbb{Z}_p^d$, the homomorphic evaluation algorithm is defined as follows: $\mathsf{Eval}(\mathbf{ct}, \mathbf{c}) \coloneqq \mathbf{c} \cdot \mathbf{ct}$.
- quadratic root detection. The algorithm for quadratic root detection can be implemented using Dec and the secret key (i.e., sk := s): decrypt the message and evaluate the polynomial, testing if it is equal to 0.
- **image verification.** Using the decryption algorithm Dec and sk , we can implement image verification (algorithm \in).

4.3 Technical Challenges

Noise growth. During the homomorphic evaluation, the noise grows as a result of the operations which are performed on the encodings. Consequently, in order to ensure that the output of Eval is a valid encoding of the expected result, we need to start with a sufficiently small noise in each of the initial encodings.

In order to bound the size of the noise, we first need a basic theorem on the tail bound of discrete Gaussian distributions due to Banaszczyk [3]:

Lemma 1 ([3]). For any $\sigma, T \in \mathbb{R}^+$ and $\mathbf{a} \in \mathbb{R}^n$:

$$\Pr[\mathbf{x} \leftarrow \chi_{\sigma}^{n} : |\mathbf{x} \cdot \mathbf{a}| \ge T\sigma \|\mathbf{a}\|] < 2 \exp(-\pi T^{2}).$$
(1)

At this point, this corollary follows:

Corollary 1. Let $\mathbf{s} \leftarrow \mathbf{z}_q^n$ be a secret key and $\mathbf{m} = (m_0, \ldots, m_{d-1}) \in \mathbf{Z}_p^d$ be a vector of messages. Let \mathbf{ct} be a vector of d fresh encodings so that $\mathbf{ct}_i \leftarrow \mathsf{E}(\mathbf{s}, m_i)$, and $\mathbf{c} \in \mathbf{Z}_p^d$ be a vector of coefficients. If $q > 2p^2 \sigma \sqrt{\frac{\kappa d}{\pi}}$, then $\mathsf{Eval}(\mathbf{c}, \mathbf{ct})$ outputs a valid encoding of $\mathbf{m} \cdot \mathbf{c}$ under the secret key \mathbf{s} .

Smudging. When computing a linear combination of encodings, the distribution of the error term in the final encoding does not result in a correctly distributed fresh encoding. The resulting error distribution depends on the coefficients used for the linear combination, and despite correctness of the decryption still holds, the error could reveal more than just the plaintext. We combine homomorphic evaluation with a technique called noise smudging [21, 2, 4], which "smudges out" any difference in the distribution that is due to the coefficients of the linear combination, thus hiding any potential information leak.

Zero-Knowledge. We now present a version of the the famous "leftover hash lemma" introduced in [29] that will be useful later when proving the zeroknowledge property of our construction. In a nutshell, it says that a random linear combination of the columns of a matrix is statistically close to a uniformly random vector, for some particular choice of coefficients.

Lemma 2 (Specialized leftover hash lemma). Let n, p, q, d be non-negative integers. Let $A \leftarrow \mathbf{z}_q^{n \times d}$, and $\mathbf{r} \leftarrow \mathbf{z}_p^d$. Then we have

$$\Delta(A, A\boldsymbol{r}), (A, \boldsymbol{u}) \leq \frac{1}{2}\sqrt{p^{-d} \cdot q^n},$$

where $A\mathbf{r}$ is computed modulo q, and $\mathbf{u} \leftarrow \mathbb{Z}_q^n$.

Practical Considerations. A single encoded value has size $(n + 1) \log q = \tilde{O}(\lambda)$. Therefore, as long as the prover sends only 2 encodings, the proof is guaranteed to be (quasi) succinct.

Although the scheme requires the noise terms to be sampled from a discrete Gaussian distribution, for practical purposes we can sample them from a bounded uniform distribution (see, e.g., [36] for a formal assessment of the hardness of LWE in this case). $\begin{array}{ll} \displaystyle \frac{(\mathsf{pk},\mathsf{st},\{m_i\},\{\mathsf{Dec}(\mathsf{ct}_j)\})\leftarrow\mathcal{D}_0(\lambda)}{(\mathsf{pk},\mathsf{sk})\leftarrow\mathsf{K}(1^{\lambda})} & \qquad \displaystyle \frac{(\mathsf{pk},\mathsf{st},\{m_i\},\{d_j\})\leftarrow\mathcal{D}_1(\lambda)}{(\mathsf{pk},\mathsf{sk})\leftarrow\mathsf{K}(1^{\lambda})} \\ (\mathsf{st},m_1,\ldots,m_d)\leftarrow\mathsf{M}(1^{\lambda}) & \qquad \displaystyle (\mathsf{st},m_1,\ldots,m_d)\leftarrow\mathsf{M}(1^{\lambda}) \\ \sigma\leftarrow(\mathsf{pk},\mathsf{E}(m_1),\ldots,\mathsf{E}(m_d)) & \qquad \displaystyle (\mathsf{a}_1,\cdots,\mathsf{a}_n,\mathsf{b})\leftarrow\mathsf{S}(\mathsf{pk};z) \\ \{\mathsf{ct}_j\}_{j=1}^n\leftarrow\mathcal{A}(\sigma;z) & \qquad \displaystyle \mathsf{a}_j,\mathsf{b}\in\mathbb{F}^d \\ \text{where }\mathsf{Dec}(\mathsf{ct}_j)\neq\perp & \qquad \displaystyle d_j\coloneqq\sum_{i=1}^d a_{ji}m_i+b_i \end{array}$

Fig. 3. Distributions \mathcal{D}_0 and \mathcal{D}_1 in Linear-Targeted Malleability.

5 Security of our zk-SNARG

Following our framework for SAP and implementing it with the encryption scheme *Enc* as described above and making some modification for the zero-knowledge, we obtain a new lattice-based zk-SNARG scheme with short proofs consisting in 2 ciphertexts instead of 5 in [20]. The soundness of the resulting SNARG scheme relies on the long-standing hardness assumption of *linear targeted malleability* of the encoding scheme.

Moreover, the same construction yields a zk-SNARK (a zero-knowledge succinct non-interactive argument of knowledge) if the soundness property is replaced with a corresponding knowledge property, and the scheme E satisfies *linear-only encryption*, where the simulator is required to be efficient (i.e., PPT). For more details, we refer to [9]. Roughly, the knowledge property states that there exists an extractor such that for every linear strategy that convinces the verifier of some statement u with high probability, the extractor outputs a witness w such that $(u, w) \in \mathcal{R}$.

5.1 Hardness Assumptions

Linear-Only Encoding Schemes. A linear-only encoding scheme is an encoding scheme where any adversary can output a valid new encoding only if this is a linear combination of some previous encodings that the adversary had as input. At high-level, a linear-only encoding scheme does not allow any other form of homomorphism than linear operations. If we require from the adversary to actually know the coefficients of the linear combination, we assume extractable linearonly, and model this knowledge by the existence of a non-black-box polynomial time extractor.

In this work, we use the weaker notion of *linear-targeted malleability*, employed also in [9]. This is closer to the definition template of Boneh et al. [12]. In such a notion, the extractor is replaced by an efficient simulator. Relying on this weaker variant, if the simulator is allowed to be inefficient, we are only able to

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prove soundness of the SNARG, though not knowledge soundness as needed for SNARKs. Concretely, the linear-only property rules out any encryption scheme where ciphertexts can be sampled obliviously; instead, the weaker notion does not, and thus allows for shorter ciphertexts.

Definition 3 (Linear-Targeted Malleability, [9]). An encoding scheme satisfies linear-targeted malleability property if for all PPT adversaries \mathcal{A} and plaintext generation algorithm M there exists a simulator S such that, for any sufficiently large $\lambda \in \mathbb{N}$, any "benign" auxiliarly input z the following two distributions $\mathcal{D}_0(\lambda), \mathcal{D}_1(\lambda)$ in figure 3 are computationally inistinguishable.

5.2 Security Proof

Before formally proving this is a SNARG, let us give a little intuition behind the different components in the scheme (see figure 1). The role of β is to ensure A and B are consistent with each other in the choice of coefficients a_0, \ldots, a_m . In the verification equation the product $A(A + \beta)$ involves a linear dependence on β , and we will later prove that this linear dependence can only be balanced out by the term B with a consistent choice of a_0, \ldots, a_m in A and B. The role of δ to make the product δB of the verification equation independent from the first product and preventing mixing and matching of elements intended for different products in the verification equation. Finally, the prover algorithm uses r to randomize the proof to get zero-knowledge.

Theorem 1. Assuming that the scheme Enc is a linear-targeted malleable encoding scheme, the protocol given in figure 1 is a non-interactive zero-knowledge argument.

Completeness. Completeness holds by direct verification.

Zero-Knowledge. To obtain a zero-knowledge protocol, we do two things: we add a smudging term to the noise of the encoding, in order to make the distribution of the final noise independent of the coefficients a_i , and we randomize the target polynomial t(x) to hide the witness from the verifier. The random vectors constituting the first element of the ciphertext are guaranteed to be statistically indistinguishable from uniformly random vectors by leftover hash lemma (cf. Lemma 2).

To see that the simulated proofs are indistinguishable from the real proofs, first observe that the simulation procedure always produces verifying proofs. Next, observe that for a given instance and proof $\pi = (A, B)$ the element Auniquely determines B through the verification equation. In a real proof the random choice of r makes the value encoded in A uniformly random, and in a simulated proof the random choice of μ makes the value inside A uniformly random. So in both cases, we get the same probability distribution over the values hidden by the encodings A, B with uniformly random A and the unique matching B.

We are left with showing that after applying our encoding scheme on these values, the two proofs, the real one and the simulated one, are statistically indistinguishable.

In both worlds, the proof is a couple of encodings (A, B). Once the vrs is fixed, each encoding can be written as $(-\mathbf{a}, \mathbf{a} \cdot \mathbf{s} + pe + m)$, for some $\mathbf{a} \in \mathbb{Z}_q^n$ and some $m \in \mathbb{Z}_p$ satisfying the verification equations. Due to Lemma 2, the random vectors \mathbf{a} are indistinguishable from uniformly random in both worlds. The error terms are statistically indistinguishable due to smudging techniques applied to the ciphertexts. The zero-knowledge follows from these claims, since the simulator can use re-randomization to ensure that its actual encodings (not just what is encoded) are appropriately uniform.

Computational Soundness. The linear-targeted malleability property of the encryption scheme constrains the prover to only use affine strategies. This ensures soundness for our SNARG. To check a proof, the verifier decrypts the prover's responses and checks the corresponding quadratic equation on these values.

What remains is to demonstrate that for any affine prover strategy that is able to produce a couple statement-proof (\mathbf{u}, π) that passes the verification test, there exists the simulator S as defined in figure 3 that outputs a valid witness **w** for the statement **u**.

For any prover algorithm $\mathcal{A}(\mathbf{crs}) \to (\mathbf{u}, \pi)$, by our linear-targeted malleability assumption the values A, B encoded in the proof $\pi = (A, B)$ are identically distributed as two simulated linear combination of some initial values $\sigma = (\delta t(s)^2, \ \beta t(s), \ \{\delta s^i\}_{i=0}^{d-1}, \ \{\delta s^i t(s)\}_{i=0}^{d-2}, \ \{\delta w_i(s) + \beta v_i(s)\}_{i=\ell+1}^m)$ encoded in the crs. More formally, we consider the simulator S that on input σ and some auxiliary input z (which corresponds to the rest of the SNARG's crs) outputs a pair of coefficients such that the resulting values c, d are indistinguishable from the values encoded by the proof π :

$$\begin{array}{ll} \displaystyle \frac{(\sigma, \mathrm{st}, \mathrm{Dec}(A), \mathrm{Dec}(B)) \leftarrow \mathcal{D}_0(\lambda)}{(\mathsf{pk}, \mathsf{sk}) \leftarrow \mathsf{K}(1^{\lambda})} & \displaystyle \frac{(\sigma, \mathrm{st}, c, d) \leftarrow \mathcal{D}_1(\lambda)}{(\mathsf{pk}, \mathsf{sk}) \leftarrow \mathsf{K}(1^{\lambda})} \\ \displaystyle (\mathrm{st}, m_1, m_2, \mathbf{m}_3, \mathbf{m}_4, \mathbf{m}_5) \leftarrow \mathsf{M}(1^{\lambda}) & \displaystyle (\mathrm{st}, m_1, \ldots, m_n) \leftarrow \mathsf{M}(1^{\lambda}) \\ \sigma \leftarrow (\mathsf{pk}, \mathsf{E}(m_1), \ldots, \mathsf{E}(\mathbf{m}_5)) & \displaystyle \mathrm{where} \ m_3, \ldots m_n \ \mathrm{is} \ \mathrm{a} \ \mathrm{reordering} \ \mathrm{of} \\ \mathrm{where} \ m_1 = \delta t(s)^2, \ m_2 = \beta t(s), & \displaystyle \mathrm{the} \ \mathrm{entries} \ \mathrm{in} \ \mathbf{m}_3, \mathbf{m}_4, \mathbf{m}_5 \\ \displaystyle \{m_{3i} = \delta s^i\}_{i=0}^{d-1}, \ \{m_{4i} = \delta s^i t(s)\}_{i=0}^{d-2}, \\ \displaystyle \{m_{5i} = \delta w_i(s) + \beta v_i(s)\}_{i=\ell+1}^m & \mathbf{c}_j, \mathbf{b}_j \in \mathbb{F}^n \\ \displaystyle (\mathbf{u}, \pi \coloneqq (A, B)) \leftarrow \mathcal{A}(\sigma; z) & \displaystyle c \coloneqq \sum_{i=1}^n c_i m_i + c' \\ \mathrm{such} \ \mathrm{that} \ \mathsf{V}(\mathsf{vrs}, \mathbf{u}, \pi) = 1 & \displaystyle b \coloneqq \sum_{i=1}^n b_i m_i + b' \end{array}$$

If \mathcal{A} convinces the verifier to accept with probability at least $\varepsilon(\lambda)$, then, with at least $\varepsilon(\lambda) - \mathsf{negl}(\lambda)$ probability, the distribution on the left satisfies that $\mathsf{V}(\mathsf{vrs}, \mathbf{u}, (\mathsf{E}(c), \mathsf{E}(b))) = 1$. However, in this distribution, the generation of $(\{m_i\})$ is independent of the generation of the simulated affine function coefficients $(c', \mathbf{c}, b', \mathbf{b})$. Therefore, by averaging, there exists some $(c', \mathbf{c}, b', \mathbf{b})$ such that, with probability at least $\varepsilon(\lambda)/2$ over the choice of $\{m_i\}_i$ it holds that $\pi' = (\mathsf{E}(c), \mathsf{E}(b))$ is a valid proof for **u**. We can use some basic linear algebra techniques to recombine the coefficients $(c', \mathbf{c}, b', \mathbf{b})$ and extract an actual witness $\mathbf{w} = (a_{\ell_u+1}, \ldots a_m)$ for the statements $\mathbf{u} = (a_1, \ldots a_{\ell_u})$.

Given that the number of variables and the lenght of the equation is significant we will not detail all the computations, but the intuition of how we recover these coefficients from the values c, b is the following:

We can rewrite c as

$$c \coloneqq c(\beta, \delta, s) = c_1 \delta t(s)^2 + c_2 \beta t(s) + c_3(s) \delta + c_4(s) \delta t(s) + \sum_{i > \ell_u} c_{5i} \left(\delta w_i(s) + \beta v_i(s) \right)$$

for known field elements $c_1, c_2, \{c_{5i}\}_{i>\ell_u}$ and polynomials $c_3(x), c_4(x)$ of degrees d-1 and d-2, respectively. We can write out $b := b(\beta, \delta, s)$ in a similar fashion from the values in σ . By the Schwartz-Zippel lemma the proof $\pi' = (c, d)$ has negligible probability to pass the check unless the verification equation $c^2 + \beta c = \delta(b + \phi(\mathbf{u}))$ holds not only for the values c, b, but for some actual polynomials $c(x_\beta, x_\delta, x_s), b(x_\beta, x_\delta, x_s)$ in indeterminates x_β, x_δ, x_s . We now view the verification equation as an equality of multivariate polynomials in x_β, x_δ, x_s and by cancelling the respective terms in this polynomial equality (for example the terms with indeterminate x_β^2 , then the ones with $x_\delta x_\beta$, etc.), we eventually remain only with the terms involving powers of x_s that should satisfy:

$$\left(\sum_{i=0}^{m} c_i v_i(x_s)\right)^2 = \sum_{i=0}^{m} c_i w_i(x_s) + b_h(x_s) t(x_s).$$

This shows that $(a_{\ell_u+1}, \ldots, a_m) = (c_{\ell_u+1}, \ldots, c_m)$ is a witness for the statement $(a_1, \ldots, a_{\ell_u})$.

Lower Bounds for SNARGs. It is an intriguing question how efficient noninteractive arguments can be and what is the minimal size of the proof. Groth showed in [27] that a pairing-based non-interactive argument with generic group algorithms must have at least two group elements in the proof. We do not have an equivalent result for lower bounds in the post-quantum setting, but recent construction aim to minimize the number of lattice-encodings in the proof and the verification overhead. Our lattice-based SNARG seems to be the most optimal to date in the size of the proof and the number of verification equations, we achieve proofs of size 2 encodings and only one verification equation.

Acknowledgements. Research funded by: the the European Research Council (ERC) under the European Unions's Horizon 2020 research and innovation programme under grant agreement No 803096 (SPEC); the Danish Independent Research Council under Grant-ID DDF-6108-00169 (FoCC).

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